1.131 DECEMPATION

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determining factor of oircult performance in apall Mismatch is a leading cause of yield loss and a mixed-signal ICs

accounts for variations in physical process Motorola has developed a new mismation model: parameters and is accurate over geometry and bias

The designer need quick, easy access to the new

The model is complex and automation is required

$$\int_{u}^{2} = \sum (\partial I_{d} / \partial p_{j})^{2} \sigma_{pj}^{2}$$

Motorola Internal Use Chly



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- Prior art based on the simplistic Felgro
- P. G. Drenhan, C. G. McAngrew "A Comprehensive" MOSPET MISMARON WOOLA," 1999 IEEE IEDM.

Does not have a proper physical foundation

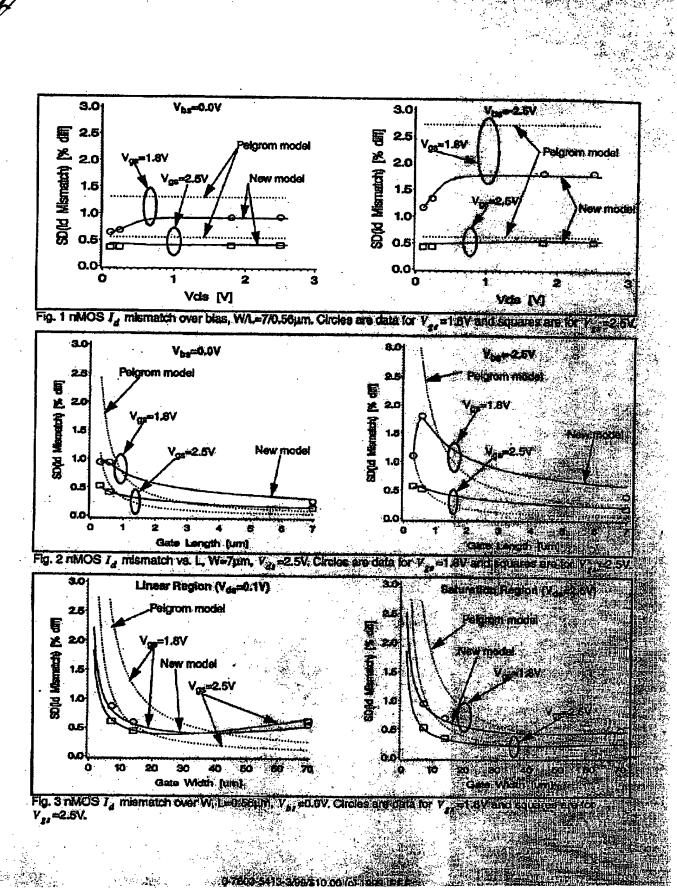
- graded channel (GCMOS & halo) and power gross errors in mismatch prediction. Prior art can't handle non-traditional devices such as
- Inferior commercial tool is available from BTA echnology

MOSFETs.

CB









- of a web-based tool Automate the Motorola Mismatch
- Three different solution types
- Current Mirror (for designers)
- Differential Pair (for designers)
- Voltage Driven (for technology developers)
- and only to Motorola Make the tool readily available throughout
- Perform single mismatch predictions
- Perform multiple mismatch predictions by allowing the designer to sweep over bias and geometry

AMSTC > > > > > > nalog Mixed Signal Technology Center

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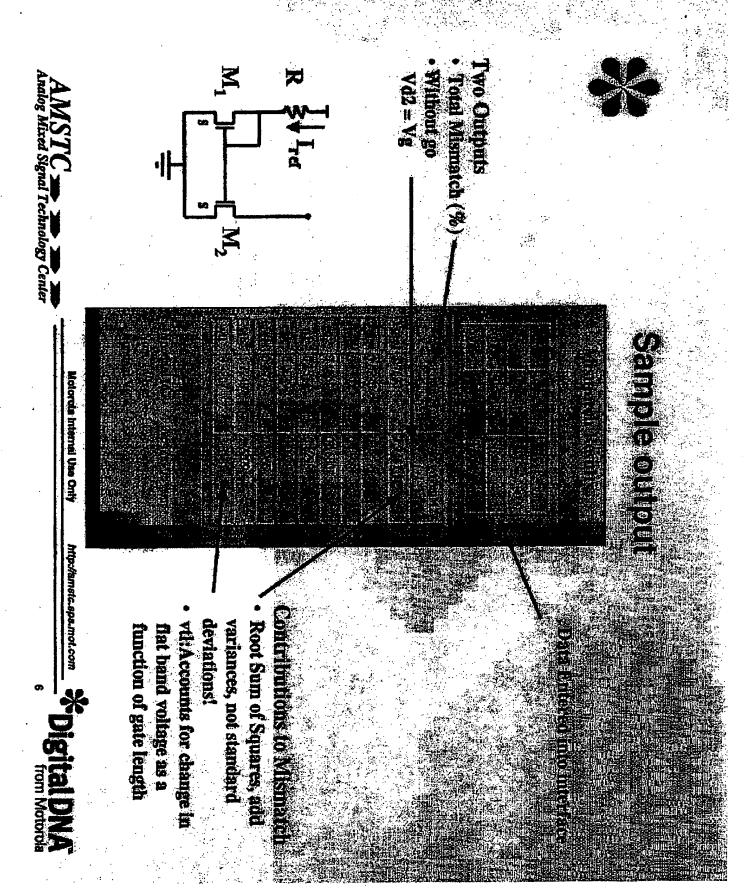
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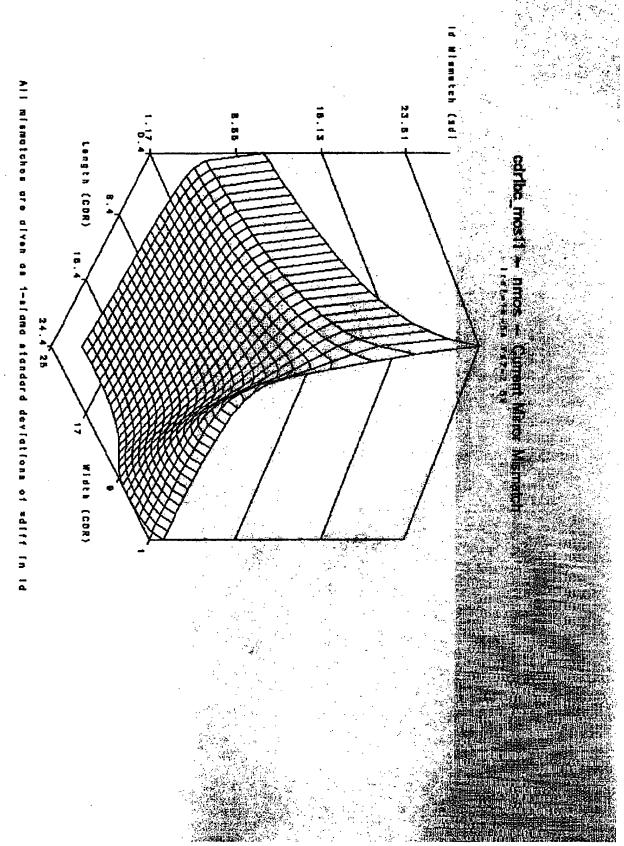




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